

Notice of References Cited	Application/Control No. 10/038,351	Applicant(s)/Patent Under Reexamination BRATT ET AL.	
	Examiner Aimee J. Li	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,381,144	01-1995	Wilson et al.	341/63
	B	US-6,188,797	02-2001	Moledina et al.	382/246
	C	US-6,912,070	06-2005	Govindaswamy et al.	341/106
	D	US-			
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

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	U	www.dictionary.com search terms: last, specify, host
	V	Molloy, Stephen and Jain, Rajeev. "Low Power VLSI Architectures for Variable-Lenth Encoding and Decoding". IEEE ©1997. Pages 997-1000.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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